

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re divisional patent application of 09/128,503 filed on August 3, 1998

Moore et al.

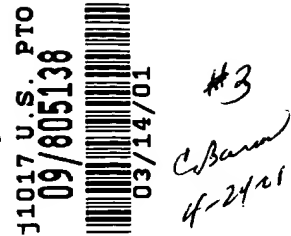
Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER
CONSUMPTION AND METHOD THEREFOR



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. All of these references were either cited or submitted in parent Application No. 09/128,503 and thus copies of these references are not provided in accordance with 37 C.F.R. §1.98(d).

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

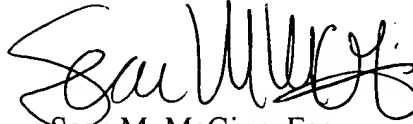
It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Rule 53(b) Divisional of Application No. 09/128,503
filed August 3, 1998

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's

Deposit Account No. 09-0456.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Sean McGinn", written over the typed name.

Sean M. McGinn, Esq.

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Customer No. 21254

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.
BU9-98-050DIV3

SERIAL NO.
Not yet assigned

Moore et al.

FILING
Concurrently herewith

GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,700,873	10/24/72	Yhap			
	3,918,030	11/04/75	Walker			
	4,145,736	03/20/79	Yamada et al.			
	4,251,862	02/17/81	Murayama			
	4,594,661	06/10/86	Moore et al.			
	4,660,171	04/21/87	Moore et al.			
	4,685,080	08/04/87	Rhodes, Jr. et al.			
	4,763,246	08/09/88	Holt et al.			
	5,084,824	01/28/92	Lam et al.			
	5,093,775	03/03/92	Grundmann et al.			
	5,185,868	02/09/93	Tran			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

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ATTY DOCKET NO.
BU9-98-056 DIV3

SERIAL NO.
Not yet assigned

Moore et al.

FILING
Concurrently herewith

GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,249,273	09/28/93	Yoshitake et al.			
	5,276,824	01/04/94	Shruhak et al.			
	5,299,321	03/29/94	Iizuka			
	5,371,864	12/06/94	Chuang			
	5,412,785	05/02/95	Skruhak et al.			
	5,426,591	06/20/95	Ginetti et al.			
	5,455,938	10/03/95	Ahmed			
	5,465,216	11/07/95	Rotem et al.			
	5,475,824	12/12/95	Grochowski et al.			
	5,491,640	02/13/96	Sharma et al.			
	5,515,302	05/07/96	Horr et al.			

FOREIGN PATENT DOCUMENTS

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						YES	NO

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GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,517,432	05/14/96	Chandra et al.			
	5,519,627	05/21/96	Mahmood et al.			
	5,568,442	10/22/96	Kowalczyk et al.			
	5,572,437	11/05/96	Rostoker et al.			
	5,581,742	12/03/96	Lin et al.			
	5,598,348	01/28/97	Rusu et al.			
	5,615,357	03/25/97	Ball			
	5,650,938	07/22/97	Bootehsaz et al.			
	5,680,332	10/21/97	Raimi et al.			
	5,732,255	03/1998	Verbauwhede	712	248	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Su et al., "Low Power Architecture Design and Compilation Techniques for High-Performance Processors", IEEE, 1994, pp. 489-498.
		Su et al., "Saving Power in the control Path of Embedded Processors", IEEE Design & Test of Computers, 1994, pp. 24-30.

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